

WI-WS-002 ??????????????
Sale Order

1.??????????

The diagram illustrates a sequence of operations on a memory stack. The stack grows downwards from higher addresses at the top to lower addresses at the bottom.
Operations shown:
1. Push operation: A new element is added to the stack.
2. Pop operation: An element is removed from the stack.
3. Comparison operation: Two elements are compared, indicated by a '-' sign.
4. Modification operation: An element is updated, indicated by a '=' sign.
5. Address calculation: The address of an element is calculated, indicated by a '+' sign.
6. Memory access: Data is read from or written to a specific memory location.
7. Branching operation: The flow of execution is altered based on a condition, indicated by a 'B' label.

2.??

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3.

3.1

3.2

3.3 () /

-
- Diagram illustrating the Back Order problem. The sequence of items is divided into batches that are shipped to orders. The last order is labeled "BACK ORDER".

3.4

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- 
-  BACK ORDER
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3.5





4.????????????

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5.

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6. ?????????????????????

image-1656826184572.png

7.????????????????????????????????

7.1





1. (WS3-F01)

2. BC

1. BC

2. “ ”

3. “ ”

4. “ ”

5. “ ” “ ”

6. “ F11 ” “ ”

7. “ ”

8. “ ”

9. “ ”

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“ SCV SHV SCN ROV

1. “ / ”

2. “ ”

3. “ Ctrl + F3 ”

4. “ ”

5. “ ”

6. “ ”

7. “ ”

8. “ ”

9. “ ”

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•

7.2

1. “ ”

2. “ ”

3. “ ”

1. “ ”

2. “ ”

(RT3-F01)

[illegible]

(F-RT-301)


 (WS3-F01)

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[illegible][illegible]

3. " "

[illegible]

5. " "

[illegible]

6. □□ " F11 " □□□□ □□□□□□□□□□ " □□□□□□□□□□□□□□□□ "

7.

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3.

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 "

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 "

4.  "  "

[illegible]

SHV □□□ □□□□□ □□□□□□□□□□□□□□□□□□

SCN

ROV   

1. " / "

2. " "

[illegible][illegible]

5.

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 "

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 "

6.

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•  2  

[illegible]

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 (Pickup Bill)

- ## Back Order

(F-WS-401)

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-
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BACK ORDER

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BACK

BC

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- BC
- | | | | |
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| | | | |
|--|--|--|--|

BCV

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BHV

Updated 27 May 2025 01:54:49 by [REDACTED]