
























???????????????? SPO
????????????

The diagram illustrates a 100-bit bus system with 10 stations (A-J) and a central SPO. Each station has a 10-bit input/output bus connected to a 100-bit system bus. The SPO is connected to the system bus via a 100-bit bus.

	
<p>1.  SPO</p>   A     <p>A</p>	<p>1.  SPO</p>   A    <p>2.  SPO</p>  A      A 

Link Back Order

image.png

image.png

Revision #7

Created 22 July 2024 02:07:25 by [redacted]

Updated 8 August 2024 06:54:48 by [redacted]