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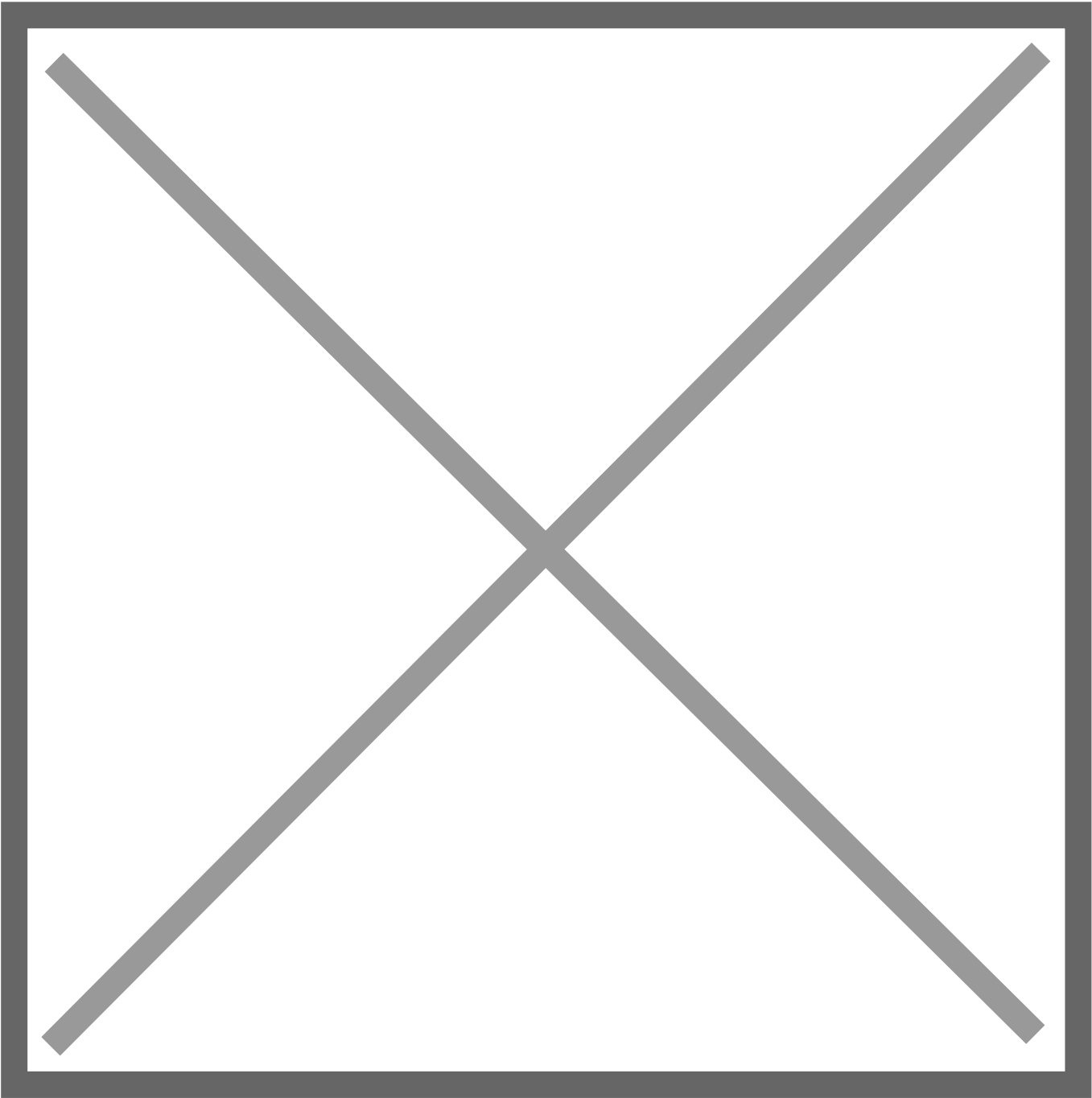
Figure 1 illustrates the layout of a 2567-bit memory array. The array is divided into 10 segments, each 256 bits long, with a total of 2567 bits. The segments are labeled 1 through 10. The total size is 2567 bits, and the segments are 256 bits each.

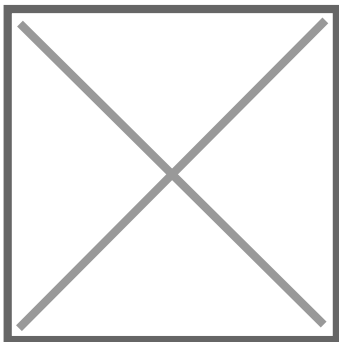
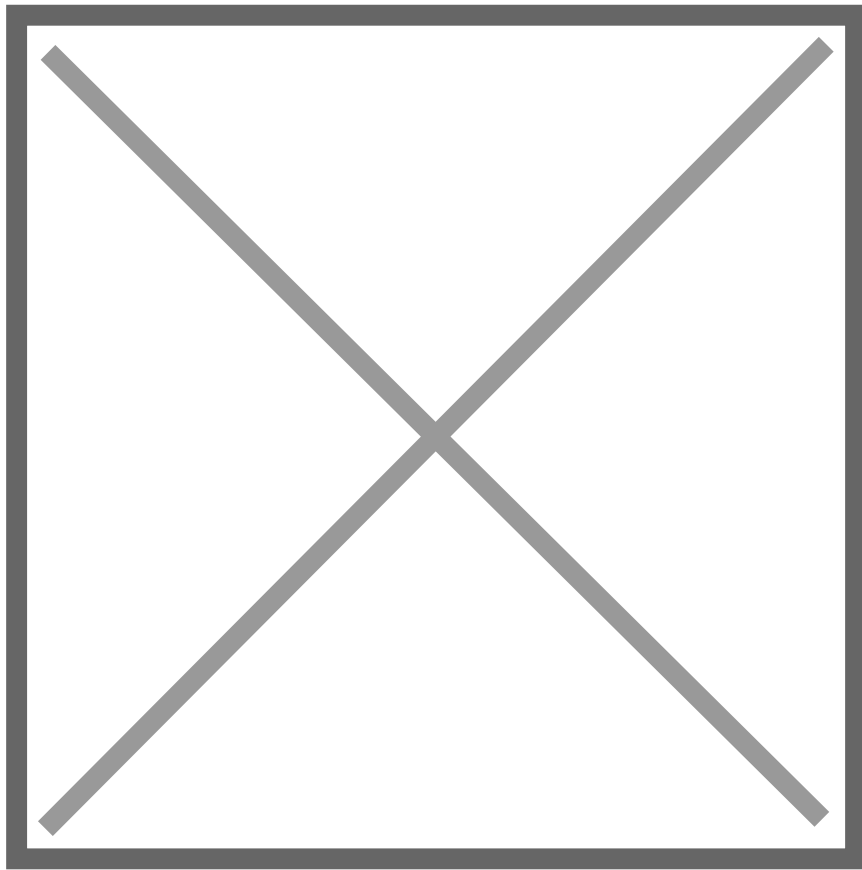
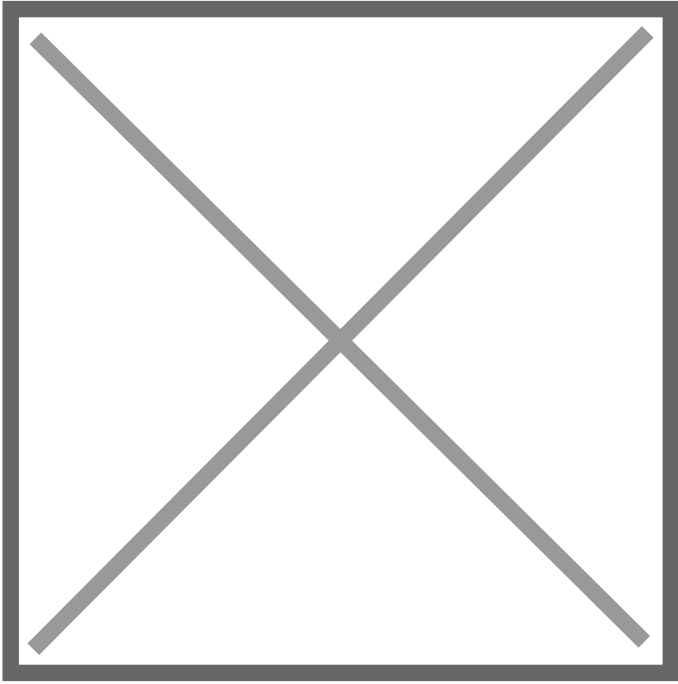
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<p>1.</p> 3 2	<p>1. VDO</p> <p>2. </p>
<p>3.</p> 	

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Revision #8

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