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The diagram illustrates the layout of a 16-bit instruction. The instruction is divided into several fields: a 16-bit register field, a 4-bit opcode field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, a 4-bit register field, and a 4-bit register field. The fields are arranged in a grid-like structure with labels and arrows indicating the flow of data.

[illegible]

Link

<https://vdo.pay9.co/videos/embed/487cc1be-5ccb-48d2-be77-5a8e5e4afbc5>



<https://vdo.pay9.co/videos/embed/72c26723-1b54-4b16-a72e-7ab01aba3133>

<https://vdo.pay9.co/videos/embed/38dbaa4a-1545-46d0-990d-a6c24ae2a0f6>

<https://vdo.pay9.co/videos/embed/8a5d22c9-720e-4959-8520-4315f2205c3e>

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