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The diagram illustrates the IEEE 754-2008 floating-point format. It shows the layout of bits for single, double, and quad precision. Single precision has 1 sign bit, 8 exponent bits, and 23 mantissa bits. Double precision has 1 sign bit, 11 exponent bits, and 52 mantissa bits. Quad precision has 1 sign bit, 15 exponent bits, and 112 mantissa bits. The diagram uses boxes to represent groups of bits and labels like 'OFS' for offset binary.

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