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- WI-MK-001 
- WI-MK-002 
- WI-MK-003 
- WI-MK-004 









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happy-young-asian-businessmen-businesswomen-meeting-brainstorming-ideas-about-ne









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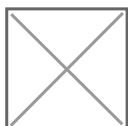
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5.6

5.6.1

5.6.2
(W-MK-101)

5.6.3
(F-MK-103)

5.7

5.7.1
(F-MK-103)

5.7.2
(R-MK-101)

5.7.3
(F-MK-104)

6. ?????????????

- F-SL-101
- F-SL-102
- F-RT-102 Department
- F-RT-103 Weekly Meeting & Actionplan
- F-RT-101 -
- Department
- R-SL-201
- W-MK-101
- F-MK-101
- F-MK-102

WI-MK-002

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beautiful-gift-voucher-with-hand.jpg

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(FREE ITEM)
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(PREMIUM)
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(PRICE WAR)

The diagram illustrates the 32-bit IEEE 754 floating-point format, which is used to represent real numbers in a computer. The format is divided into three main fields:





- Sign (s):** A single bit (1 or 0) indicating the sign of the number.
- Exponent (E):** An 8-bit field representing the power of 2. It is stored in a biased form, where the actual exponent is the stored value minus 127.
- Mantissa (M):** A 23-bit field representing the fractional part of the number. It is split into two sub-fields:
 - Mint (Integer part):** The first 2 bits of the mantissa, representing the integer part of the normalized mantissa.
 - Mfrac (Fraction part):** The remaining 21 bits of the mantissa, representing the fractional part of the normalized mantissa.

The diagram shows how these fields are used to represent a floating-point number. For example, a 20-bit mantissa is shown with a 2-bit integer part and a 18-bit fraction part. The diagram also shows the conversion of a decimal number to a floating-point representation, including the steps of normalizing the mantissa and adjusting the exponent.

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DeepinScreenshot_select-area_20220706154307.png

DeepinScreenshot_select-area_20220706154351.png

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- [100] (PRICE WAR) [100]
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STOCK CARD

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WI-MK-003

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logo-nopadol-02.png

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01.jpg

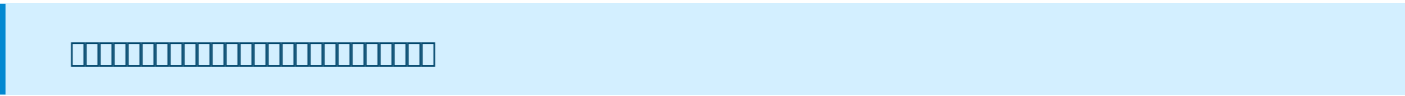
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“ [Download Logo >>](#)
<https://drive.google.com/file/d/1S7CH9kXUo7NkolaAmeDlaxRsGVSMBJ9e/view>



4.????????????????

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3	1,000,001-2,000,000	□□□□□	1,800
4	2,000,001-5,000,000	□□□□□	2,500
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