

WI-SA1-013 ??????????
(Reservation)

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1. ???????????

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- The diagram illustrates a 16-bit adder-subtractor circuit. It takes two 16-bit inputs, A and B, and a control signal S. The circuit uses a 16-bit ripple-carry adder. When S=0, the circuit performs addition (A + B). When S=1, the circuit performs subtraction (A - B) by adding the two's complement of B. The two's complement of B is generated by inverting all bits of B and adding 1. The carry-in to the least significant bit (LSB) is 0 when S=0 and 1 when S=1. The output is a 16-bit result.

4. ?????????????????

- [illegible]

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