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The diagram illustrates a 256-bit bus architecture. At the top, a horizontal line represents the bus, with several rectangular blocks connected to it, representing different components or data sources. Below the bus, a large rectangular block is shown, representing a memory or storage unit. The bus is labeled with '256' and 'bits', indicating its width. The diagram shows the flow of data between the bus and the memory unit, with arrows indicating the direction of data transfer.

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